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Application Number	10/773,968
Filing Date	February 6, 2004
First Named Inventor	Paul D. Shirley
Art Unit	
Examiner Name	
Attorney Docket Number	MIO 0112 PA/40509.272/00-0265

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NON PATENT LITERATURE DOCUMENTS

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		MICHAEL QUIRK AND JULIAN SERDA, Semiconductor Manufacturing Technology, November 11, 2003, Pages 357 - 360 Chapter 13, Prentice Hall, Upper Saddle River, New Jersey and Columbus, Ohio	

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